



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Randy B. Osborne

Application No.: 10/010,994

Filed: November 12, 2001

For: METHOD AND APPARATUS FOR
READ LAUNCH OPTIMIZATIONS
IN MEMORY INTERCONNECTCommissioner for Patents
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Examiner: Unknown

Art Unit: 2176

Sir:

Enclosed is a copy of the Notice of Publication of Application dated May 15, 2003, in connection with the above-identified patent application. The Atty Docket No. on the Notice is incorrect. The correct Atty Docket No. is 042390.P12472. All correspondence with the PTO for the above captioned matter has referenced that same Atty Docket No.

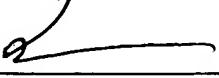
Please update your records to reflect the above information and then forward a corrected Notice of Publication of Application to us. The enclosed copy of the Notice is marked with the appropriate correction(s).

If you have any questions, please contact the undersigned.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 6/2/93

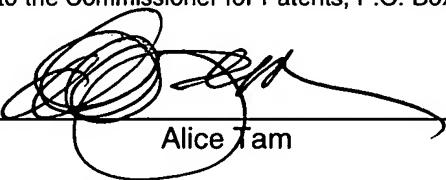

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10/010,994	11/12/2001	Randy B. Osborne	004087-P12472

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R E C E I V E D

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Title: Method and apparatus for read launch optimizations in memory interconnect

Publication No. US-2003-0093631-A1

Publication Date: 05/15/2003

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
LOS ANGELES

Date Mailed: 05/15/2003

NOTICE OF PUBLICATION OF APPLICATION

The above-identified application will be electronically published as a patent application publication pursuant to 37 CFR 1.211, et seq. The patent application publication number and publication date are set forth above.

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